

[HW2004, Set 1 solution

[> **restart;**

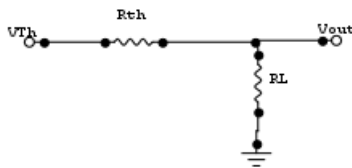
[> **i:=V_in/(R1+R2);**

$$i := \frac{V_{in}}{R1 + R2}$$

[> **V_out:=i*R2;**

$$V_{out} := \frac{V_{in} R2}{R1 + R2}$$

We know from class how to construct a Thevenin's equivalent circuit for this voltage divider circuit, so we can simplify the circuit to:



where

[> **V_th:=V_in*R2/(R1+R2);**

$$V_{th} := \frac{V_{in} R2}{R1 + R2}$$

and

[> **R_th:=R1*R2/(R1+R2);**

$$R_{th} := \frac{R1 R2}{R1 + R2}$$

Adding R_L to the output forms a voltage divider between R_{th} and R_L , so for the loaded circuit,

[> **V_out:=V_th*RL/(R_th+RL);**

$$V_{out} := \frac{V_{in} R2 RL}{(R1 + R2) \left(\frac{R1 R2}{R1 + R2} + RL \right)}$$

[In the no-load condition, V_{out} is just V_{th} , so we are looking for the R_L that gives $V_{out} = V_{th}/2$:

[> **solve(V_out=V_th/2,RL);**

$$\frac{R1 R2}{R1 + R2}$$

[This just the voltage divider output impedance, R_{th} .

[>

[Now we need to find the R_L that draws maximum power P_L . First we need to express P_L in terms of the circuit parameters. V_{out} is the voltage across R_L , and let's call I_L the current through R_L . Then

[> **PL:=IL*V_out;**

$$PL := \frac{IL V_{in} R2 RL}{(R1 + R2) \left(\frac{R1 R2}{R1 + R2} + RL \right)}$$

> **IL:=V_out/RL;**

$$IL := \frac{V_{in} R2}{(R1 + R2) \left(\frac{R1 R2}{R1 + R2} + RL \right)}$$

So the power is

> **PL;**

$$\frac{V_{in}^2 R2^2 RL}{(R1 + R2)^2 \left(\frac{R1 R2}{R1 + R2} + RL \right)^2}$$

> **simplify(%);**

$$\frac{RL R2^2 V_{in}^2}{(R1 R2 + RL R1 + RL R2)^2}$$

We want to find the RL that draws max power, so differentiate PL with respect to RL and set it equal to zero.

> **equation1:= 0=diff(PL,RL);**

$$equation1 := 0 = - \frac{2 V_{in}^2 R2^2 RL}{(R1 + R2)^2 \left(\frac{R1 R2}{R1 + R2} + RL \right)^3} + \frac{V_{in}^2 R2^2}{(R1 + R2)^2 \left(\frac{R1 R2}{R1 + R2} + RL \right)^2}$$

> **solve(equation1,RL);**

$$\frac{R1 R2}{R1 + R2}$$

This is just R_th, the output impedance of the original voltage divider. This is an important general result. You draw the maximum power from a circuit when the load matches the output impedance.

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